

Reconfigurable K-Best Algorithm for MIMO Detection Systems

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Abstract—The maximum likelihood (ML) detection is the optimal detection method for multiple-input multiple-output (MIMO) communication systems. The normal K-best Sphere Decoding Algorithm (SDA) can guarantee a fixed throughput, but it induces a large bit error rate (BER) degradation. In order to achieve close-to-ML performance, the K value needs to be sufficiently large. Thus, it needs large computation with long latency and low throughput. In this paper, a reconfigurable K-best SDA is proposed by using fixed K values for different layers of K-best detection, which can increase the MIMO detection performance. The architecture of 4×4 MIMO detector is designed for both 16QAM and 64QAM modulation.

Index Terms—MIMO, Detection, K-Best, Sphere Decoding, Maximum Likelihood

I. INTRODUCTION

Multiple-input multiple-output (MIMO) systems have interested considerable research attentions in the wireless communication field. Recently, there have been many studies on practical techniques to realize the high channel capacity in MIMO systems. It has been shown in [1] that high data rate wireless communication near Gb/s can be achieved in wireless local area networks (WLANs). The main challenge of the receiver design for MIMO systems lies in the detection of disorderly constellation. The MIMO techniques can basically be classified into space time coding (STC) and space division multiplexing (SDM) groups. STC increases the channel capacity of the wireless communication system by coding over different transmitter channels [2], whereas SDM employs multiple transmit and receive antennas and simultaneously transmits parallel data streams over all available spatial channels [3]. Especially, the high throughput technology has been discussed in SDM systems.

The SDM technology can be classified into linear and nonlinear detection methods [4]. Linear detection methods invert the channel matrix using a zero-forcing (ZF) or minimum mean squared error (MMSE) criterion. The drawback is, in general, a rather poor bit-error-rate (BER) performance. The vertical Bell Laboratories layered space-time (V-BLAST) algorithm is one of the nonlinear detection, which uses ordered successive interference cancellation decoders to improve the performances. Maximum likelihood (ML)

algorithm is another nonlinear detection scheme and minimizes the BER. However, using optimal ML detection, the operational complexity is much higher, when we adopt higher modulation constellations and antenna numbers.

Therefore, the sphere decoder (SD) algorithm has been introduced in [5], [6] to drastically reduce the operational complexity for MIMO detection. The SD algorithms have other kinds of implementation strategies, i.e., K-best SD [7] and K-best Schnorr-Euchner (SE) [8], [9].

In this paper, we propose a novel way to determine K value at each detected layer which is useful to improve normal K-best detector performance. We design pipelined VLSI architecture of our improved 4×4 MIMO detector for different modulation with much less computational complexity. Our proposed work can overcome the control path which is different from normal K-best detector.

This paper is organized as follows. Section 2 describes the MIMO detection including system model and improved detection method. Architecture design results for K-best detector are given in Section 3. Section 4 shows simulation results, and Section 5 concludes the paper.

II. MIMO DETECTION

A. MIMO System Model

To increase the data rate of the wireless communication, the spatial multiplex is used in the MIMO system with N_t transmitted antennas and N_r received antennas. The baseband equivalent model can be described in Eq. (1).

$$\mathbf{Y} = \mathbf{H}\mathbf{S} + \mathbf{n} \quad (1)$$

At each symbol time, a vector $\mathbf{S} = [s_1, s_2, \dots, s_{N_t}]^T$ with each symbol belonging to the q quadrature amplitude modulation (q -QAM) constellation passes through the channel response $N_r \times N_t$ matrix \mathbf{H} . The received vector $\mathbf{Y} = [y_1, y_2, \dots, y_{N_r}]^T$ at the receiving antenna for each symbol time is a noisy superimposition of the N_t signals contaminated by additive white Gaussian noise (AWGN).

The complex matrix Eq. (1) can be transformed to its real matrix representation as Eq. (2).

$$\begin{bmatrix} \Re(\mathbf{Y}) \\ \Im(\mathbf{Y}) \end{bmatrix} = \begin{bmatrix} \Re(\mathbf{H}) & -\Im(\mathbf{H}) \\ \Im(\mathbf{H}) & \Re(\mathbf{H}) \end{bmatrix} \begin{bmatrix} \Re(\mathbf{S}) \\ \Im(\mathbf{S}) \end{bmatrix} + \begin{bmatrix} \Re(\mathbf{n}) \\ \Im(\mathbf{n}) \end{bmatrix} \quad (2)$$

where \mathbf{R} is upper triangular matrix and \mathbf{Q} is unitary matrix.

B. K-Best Sphere Decoding Algorithm

The ML detector is the optimum detection algorithm for the MIMO system. It requires finding the signal point \mathbf{S} from all transmit vector signal set that minimizes the Euclidean distance with respect to the received signal vector \mathbf{y} with QR-decompositions as given in Eq. (4):

$$\hat{s} = \arg \min \|\mathbf{Y} - \mathbf{H}\mathbf{S}\|^2 = \arg \min \|\hat{\mathbf{y}} - \mathbf{R}\mathbf{S}\|^2 \quad (4)$$

Expanding the vector norm in Eq. (4) yields into Eq. (5):

$$\hat{s} = \arg \min \sum_{i=1}^{N_t} \left\| \hat{y}_i - R_{ii}S_i - \sum_{j=i+1}^{N_t} R_{ij}S_j \right\|^2 \quad (5)$$

The detection process starts from the last layers $l=N_t$ and works the way until the first layer is detected. It is to perform an exhaustive search of all possible combinations of the transmitted symbols that minimizes $\|\mathbf{Y} - \mathbf{H}\mathbf{S}\|^2$.

The branch cost function associated with nodes in the i -th layer is:

$$T_i(s^i) = T_{i+1}(s^{i+1}) + |e_i(s^i)|^2$$

with $e_i(s^i) = \hat{y}_i - R_{ii}S_i - \sum_{j=i+1}^{N_t} R_{ij}S_j \quad (6)$

Each node in the tree corresponds to a so-called partial Euclidean distance (PED) $T_i(s^{(i)})$, where $T_{N_t+1}(s^{(N_t+1)}) = 0$ and term $|e_i(s^{(i)})|^2$ denotes the distance increment between two successive nodes in the tree.

At each layer, K-best detector approximates a breadth-first search by keeping only K candidates with the smallest PEDs are kept for the next level search.

C. Reconfigurable K-Best SDA

Figure 1 shows the simulation results, which compare the performance of MLD, SDA, normal 2-best SDA, normal 5-best SDA at different signal to noise ratio (SNR). We can see the BER performance of the normal 5-best SDA worse than the SDA and MLD. Based on the simulation results, when K is very small, the performance significantly decreases. For example, $K=2$ in the figure. Thus, how to choose appropriate K value is very important.

In [9], a dynamic K-best SDA method was introduced. However, such dynamic K-best SDA does not have regularity to adapt K value in each detected layer.

We proposed a fixed way that has regularity to select the K value at different detection layers for different modulations as given in Eq. (7):

$$k_N = \text{int} \left[\frac{\alpha \times R_c \times L \times N}{\sqrt{E_b/N_0}} \right], \quad N \in \{N_r, N_r - 1, N_r - 2, \dots, 1\} \quad (7)$$

where L is the number of bits per symbol, R_c is the code rate, N is the current detection layer, α is adjusted to control the K value constraint flexibly and adaptively and int here means that we just round it to the nearest tens.

The value of α is assumed by considering the effect of our work performance and hardware design complexity. The number of node search decreases from detected layer N_r to layer 1. Increasing K value at early stages can reduce the possibility of missing the possible ML solution, and the performance is increased. In the system with a low SNR we will have a larger K value, since it is beneficial to reduce missing possible candidates. The analysis of K value for different detection layer is shown in Fig. 2.

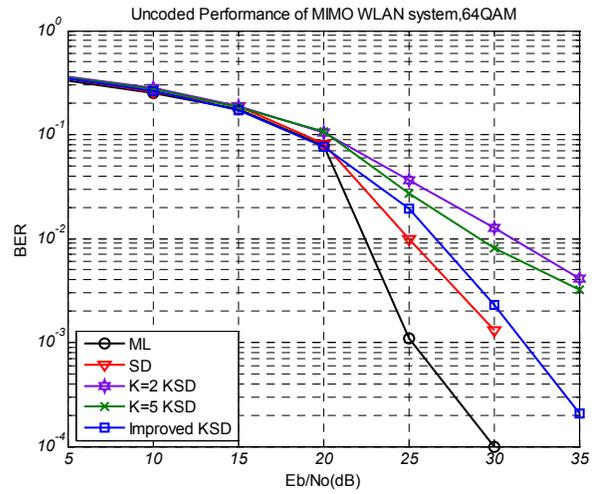


Figure 1. Performance Comparisons of ML, SDA, 2-Best SDA, 5-Best SDA, Improved KSDA ($N_r=N_t=4$, 64QAM).

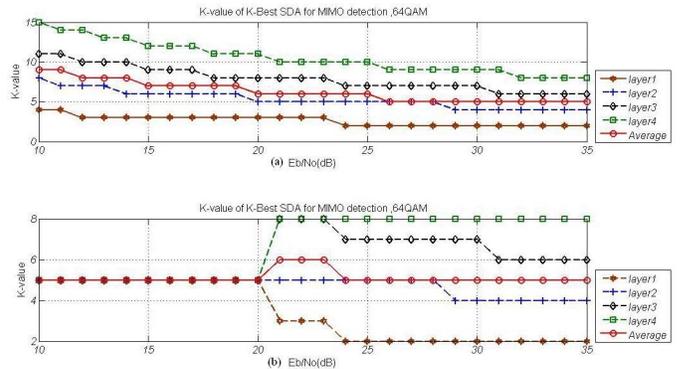


Figure 2. Analysis of K-value for different detection layer.

III. ARCHITECTURE

The proposed architecture of the K-best detector is designed to reduce the computation complexity, which is a useful attribute for 16QAM and 64QAM modulation. The flowchart of the proposed detector is shown in Figure 3. To start with the channel estimation, we can get current channel information \mathbf{H} . Then the received vector \mathbf{Y} was preprocessed by QR decomposition to get the vector $\hat{\mathbf{y}}$ and the signal \mathbf{R} , which is the element of the upper triangular matrix of channel preprocessing. At the same time, we use \mathbf{H} to estimate current SNR value and apply it to calculate K for each detected layer. The shaded portion is the main K-best detector module, which has N_r pipelined stage. For each stage, there are two sub modules: PED Computation Unit and Sorting Unit. The i -th stage receives K smallest PEDs (T_i) from the preceding stage and expands each parent nodes to all their children nodes. Then data vectors \mathcal{S}_i consist of admissible nodes. Finally, we can determine the detected vector $\hat{\mathbf{s}}$.

From the block diagram in Fig. 3, the main operation blocks of PED computation unit and sorting unit are clearly described. Figure 4 shows the architecture of PED computation unit, which includes constellation multipliers for 16QAM or 64QAM, adders, and l -norm operations. The PED block computes the PEDs of all associated children of the parent node. The different QAM computing can be changed by the QAM select control line to achieve hardware flexibility.

The role of the sorting unit in this architecture is to sort all the generated PEDs and select the smallest K PEDs. The most challenging aspect of the design of a sorting unit is to reduce the number of operation times in the architecture. The dataflow graph of a functional unit for the machine is shown in Fig. 5. The sorting method adopts a bubble scheme operation as shown in Fig.5-(a). The bubble scheme architecture approaches the latency at $\sum_{j=1}^K(N-j)$, where N denotes the constellation of the PEDs output and K is the number of K-best. The dataflow graph has feedback, because the contents of a memory cell can be written back to the cell as shown in Fig. 5-(b).

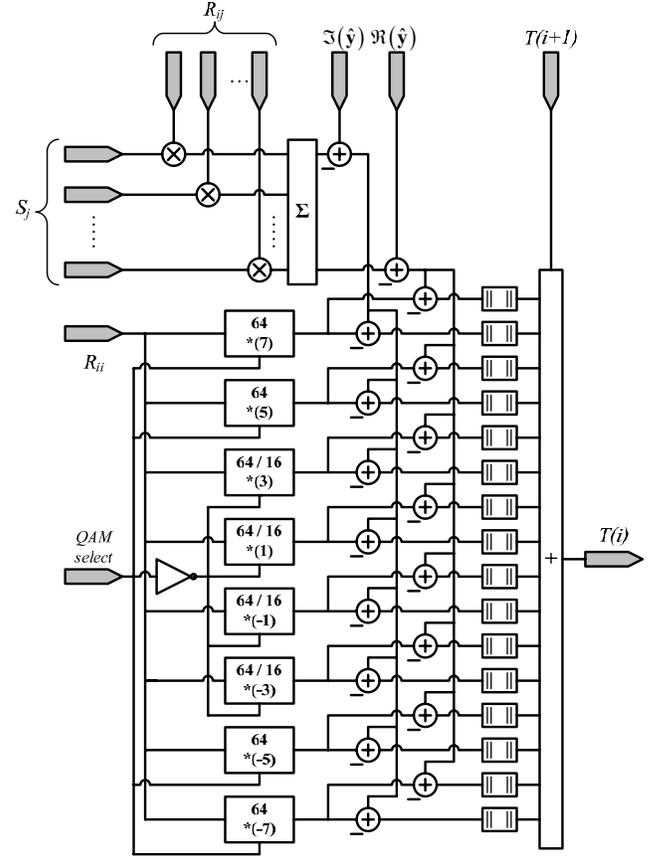


Figure 4. Architecture of PED computation unit.

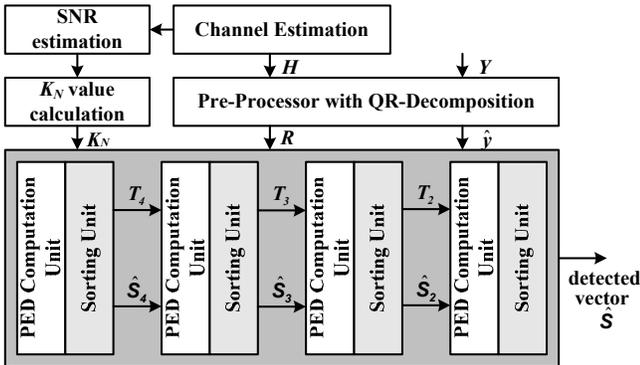


Figure 3. The flowchart of proposed K-best detector.

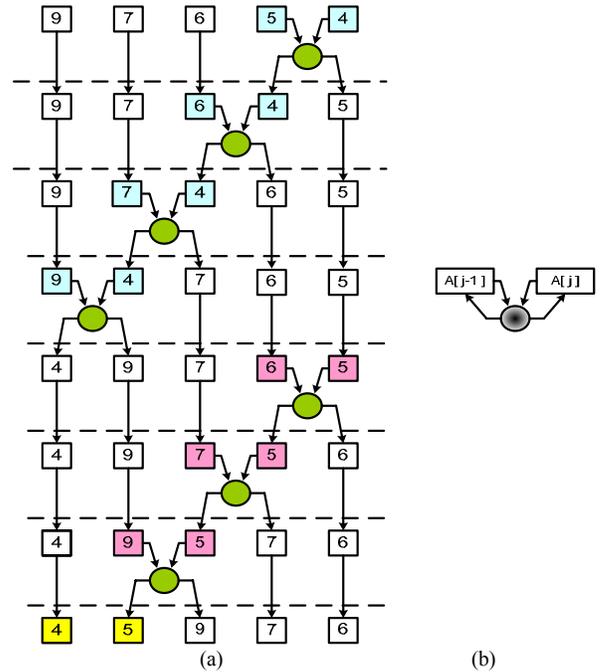


Figure 5. Architecture of Sorting Unit : (a) Data flow graph , and (b)baseline functional unit.

IV. SIMULATION RESULTS

One analysis of different α values is shown in Fig. 6, while we adopt α value from 1 to 3 with and without limit. According to this analysis, $\alpha=2$ can be adopted to obtain better performance and lower hardware architecture when we use the proposed K-best algorithm to detect 4×4 MIMO system. The limit parameter is to limit the K-best number at eight for decreasing hardware complexity.

Fig. 7 compares 16QAM with 64QAM while using the proposed K-best algorithm at the receiver of MIMO systems. For comparison, this figure also shows the performance over the AWGN with multi-antennas $N_r=N_t=4$ and without channel coding. Figure 7 clearly indicates that the 16QAM can outperform the 64QAM in MIMO detection.

V. CONCLUSION

In the detection of MIMO systems, a K-best sphere decoding is designed for the receiver, which can improve the MIMO detection performance. This paper proposes a MIMO detector hardware structure and analyzes the word-length required to obtain sufficient performance for hardware implementation. Simulation results indicate that the proposed K-best rule is better than that of other existing normal K-best algorithm.

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REFERENCES

- [1] A.J. Paulraj, D.A. Gore, R.U. Nabar, and H. Bolcskei, "An overview of MIMO communications - a key to gigabit wireless," *Proc. IEEE*, vol. 92, pp. 198–218, Feb. 2004.
- [2] S.M. Alamouti, "A simple transmit diversity technique for wireless communications," *IEEE J. Sel. Areas Commun.*, vol. 16, pp. 1451–1458, Oct. 1998.
- [3] P.W. Wolniansky, G.J. Foschini, G.D. Golden, and R.A. Valenzuela, "V-BLAST: an architecture for realizing very high data rates over the rich-scattering wireless channel," in *Proc. Int. Symp. Signal Syst. Electron.*, pp. 295–300, Oct. 1998.
- [4] A. Burg, M. Borgmann, M. Wenk, M. Zellweger, W. Fichtner, and H. Bolcskei, "VLSI implementation of MIMO detection using the sphere decoding," *IEEE J. Solid-State Circuits*, vol. 40, pp. 1566–1577, July 2005.
- [5] A. Burg, M. Wenk, M. Zellweger, M. Wegmueller, N. Felber and W. Fichtner, "VLSI implementation of the sphere decoding

Algorithm," in *Proc. ESSCIRC-2004*, Leuven, Belgium, pp. 303–306, Sep. 2004.

- [6] J. Jalden and B. Ottersten, "On the complexity of sphere decoding in digital communications," *IEEE Trans. Signal Process.*, vol. 53, pp. 1474–1484, Apr. 2005.
- [7] K.-W. Wong, C.-Y. Tsui, R.S.-K. Cheng, and W.-H. Mow, "A VLSI architecture of a K-best lattice decoding algorithm for MIMO channels," in *Proc. IEEE Int. Symp. Circuits Syst.*, pp. III-273–III-276, May 2002.
- [8] Z. Guo and P. Nilsson, "Algorithm and implementation of the K-best sphere decoding for MIMO detection," *IEEE J. Sel. Areas Commun.*, vol. 24, pp. 491–503, Mar. 2006.
- [9] Q. Li and Z. Wang, "Improved K-Best Sphere Decoding Architecture for MIMO Systems," in *Proc. IEEE Int. Symp. Circuits Syst.*, pp. 1159–1162, May 2006.

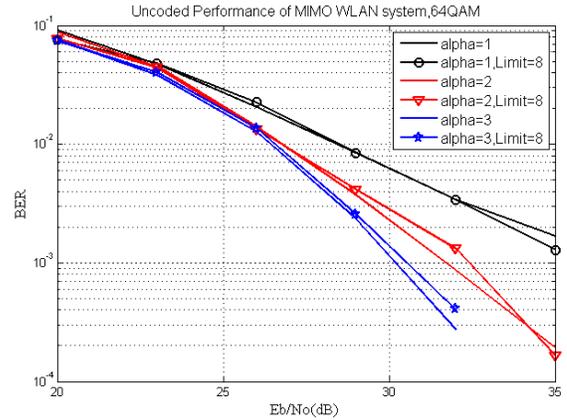


Figure 6. The uncoded BER simulation with different α values form 1 to 2.5 of MIMO system ($N_r=N_t=4$, 64QAM).

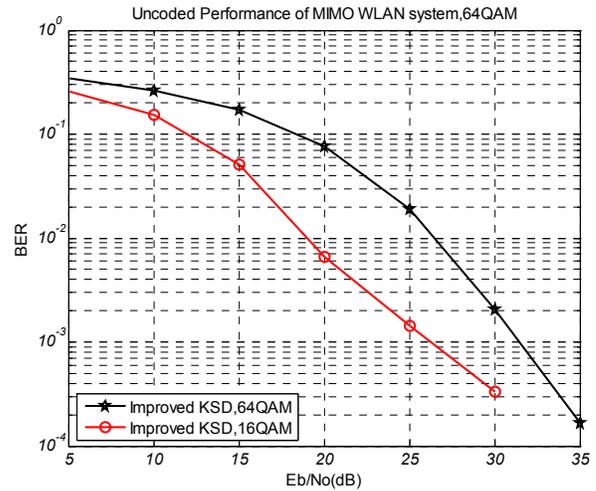


Figure 7. Simulation result of proposed K-best algorithm with 16QAM and 64QAM modulation in uncoded MIMO system ($N_r=N_t=4$).